

In the Claims

Please add new claims 10-16 as follows:

a 10. (NEW) A logic gate, as set forth in claim 6, wherein said intrinsic transistor is a p-type transistor.

11. (NEW) A logic gate, comprising:

a low noise current source coupled between a first terminal of a voltage supply and an output terminal, said low noise current source including a p-type transistor being capable of delivering a preselected voltage signal to said output terminal having a magnitude responsive to a first control signal relatively independent of the magnitude of the voltage on said first terminal of said voltage supply; and
at least one switching element coupled between the output terminal and a second terminal of the voltage supply, said switching element being capable of coupling said output terminal to said second terminal of said voltage supply in response to receiving a control signal.

12. (NEW) A logic gate, as set forth in claim 11, wherein said low noise current source includes a resistor serially coupled with the p-type transistor between the first terminal of the voltage supply and the output terminal, the transistor having a gate capable of receiving the first control signal.

13. (NEW) A logic gate, as set forth in claim 11, wherein said p-type transistor is an intrinsic transistor.